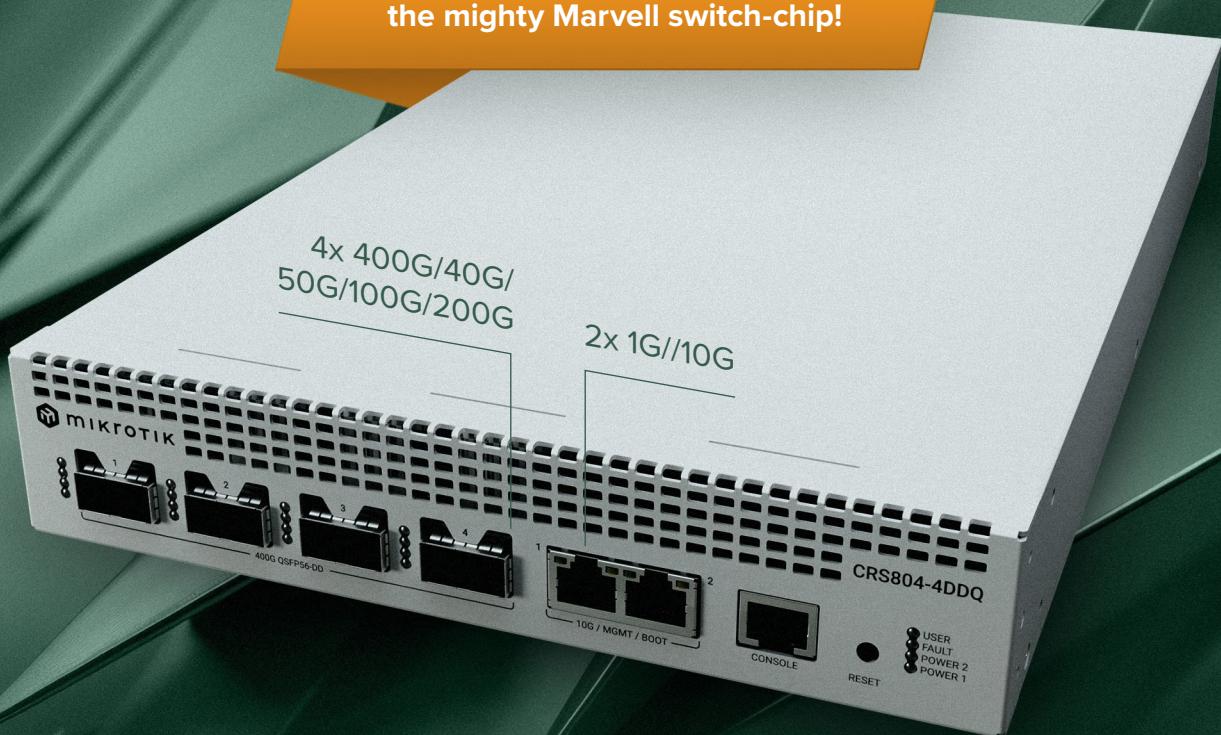




CRS804 DDQ

Compact 400G switching for AI clusters, storage fabrics, and lightning fast aggregation – for the best price on the ultra high-speed market.

Reach wire-speed on all ports with the mighty Marvell switch-chip!



CRS804 DDQ is the practical and cost-effective approach to 400 Gigabit speeds, AI, and future-proofing.



4 GB of RAM



Quad-core
2 GHz ARM CPU



Dual-redundant
hot-swap power
supplies



2x hot-swap
cooling fans



RouterOS v7

The CRS804 DDQ is a compact and reliable **400 Gigabit switch** designed for **modern AI workloads** and next-generation network upgrades. With **four 400G QSFP-DD** ports and dual 1/10G Ethernet, it delivers exceptional bandwidth density in a quiet and efficient 1U form factor.

Inside, a Marvell 98DX7335 switch chip works together with a hefty quad-core 2.0 GHz ARM CPU, 4 GB of DDR4 RAM, and RouterOS v7. This provides advanced traffic control, high performance switching, and a familiar management environment trusted by users worldwide.

The CRS804 DDQ is designed for long-term reliability and low cost of ownership, supported by hot-swap power supplies (included!), robust cooling, and low power consumption that reaches only 123 W at full load with optics installed.



  Check out our YouTube and TikTok videos for this product!

Where does it fit

AI and GPU clusters

A perfect building block for small to medium AI installations. It delivers predictable, low-latency performance and full wire-speed switching, keeping GPU nodes and storage traffic flowing without bottlenecks. The compact design and low power usage make it suitable for AI labs, research environments, and smaller rack deployments.

All-flash storage fabrics

Use native 400G ports for fast storage uplinks or split them into dual 200G links. Ideal for media production, scientific workloads, and any environment where rapid data movement is essential.

Future proofing for existing networks

The CRS804 DDQ can act as a high speed core or aggregation switch even in older racks. Each 400G QSFP-DD port is built from eight independent data lanes. This enables flexible breakout options, including two 200G links for AI workloads or lower-speed connections such as 100G or 40G. You can deploy the CRS804 DDQ in your existing network today and upgrade to higher speeds over time without replacing the switch. With the right breakout cables, the CRS804 DDQ can interconnect virtually any device with SFP or QSFP ports, regardless of generation.

Compact 400G fabrics in limited rack space

Create a powerful spine or backbone fabric using four 400G ports. Perfect for small datacenters, enterprise pods, edge compute installations, and environments where traditional 400G equipment would be too loud, too large, or too expensive.

CRS804 interface speed support:

2x
1G/10G
Ethernet ports



4x
40G/50G/100G/200G/
400G QSFP-DD ports *

* QSFP-DD ports also support break-out modes to 1G/2.5G/5G/10G/25G/50G



We have included the RMK-2x10/19 accessory, so you can mount two CRS804 switches in a single 1U slot of a standard 19-inch rack.



Accessories

Alongside CRS804, we're introducing a set of ready-to-go 400G accessories:



DDQ+DA0001

400G DAC, 1 m



DDQ+DA00031

400G DAC, 3 m



DDQ+85MP01D

400G 100 m optical transceiver



**48V DC telecom-IN
hot-swap power supply**



G1394-0350WNA



MT-HotSwapFan-V2

With CRS804, 400 Gigabits is no longer out of reach – it's an affordable way to launch your network into the future, while keeping your existing gear in the loop!



• Specifications

Product code	CRS804-4DDQ-hRM
CPU	Quad-Core AL52400 2000 MHz
CPU architecture	ARM 64bit
Size of RAM	4 GB
RAM type	DDR4
Storage	512 MB, NAND
Number of 1G/2.5G/5G/10G Ethernet ports	2
Number of 400G QSFP56-DD ports	4
Switch chip model	98DX7335
Operating system	RouterOS v7, License level 6
Operating temperature	-10°C to +60°C

• Powering

Number of AC inputs	2
AC input range	100-240 V
Power adapter nominal voltage	12 V
Power adapter nominal current	27.5 A
Max power consumption (without attachments)	92 W
Max power consumption	123 W

• Included parts

